

Best Student Paper Award Finalists

One of the missions of RFIC Symposium is to promote academic research and education. As part of the Best Student Paper Award program, several finalists are nominated every year by RFIC Technical Program Committee to enter the final contest where the top-three best papers are selected. All finalists benefit from a complimentary RFIC registration. The top-three Best Student Papers will be announced during the RFIC Plenary Session on 22 May 2016 in San Francisco. Each winner will receive an honorarium and a plaque. This year's Best Student Paper Award finalists are:

A Multiphase Switched Capacitor Power Amplifier in 130nm CMOS

Wen Yuan, Jeffrey S. Walling
University of Utah, USA
RMO3D-4 14:30

An Efficient 210GHz Compact Harmonic Oscillator with 1.4dBm Peak Output Power and 10.6% Tuning Range in 130nm BiCMOS

Chen Jiang¹, Andreia Cathelin², Ehsan Afshari¹
¹Cornell University, USA, ²STMicroelectronics, France
RMO3C-5 14:50

A Mixer Frontend for a Four-Channel Modulated Wideband Converter with 62dB Blocker Rejection

Douglas Adams¹, Yonina Eldar², Boris Murmann¹
¹Stanford University, USA, ²Technion, Israel
RTU1A-4 09:00

A 2.22–2.92GHz LC-VCO Demonstrated with an Integrated Magnetic-Enhanced Inductor in 180nm SOI CMOS

Rui Ma¹, Fei Lu¹, Qi Chen¹, Chenkun Wang¹, Feng Liu², Wanghui Zou³, Albert Wang¹
¹University of California, Riverside, USA, ²Wuhan University, China, ³Hunan University, China
RMO2A-3 10:50

An 802.11 a/b/g/n Digital Fractional-N PLL with Automatic TDC Linearity Calibration for Spur Cancellation

Dongyi Liao¹, Hechen Wang¹, Fa Foster Dai¹, Yang Xu², Roc Berenguer³
¹Auburn University, USA, ²Illinois Institute of Technology, USA, ³Innophase, USA
RMO2C-5 11:30

A Wideband Voltage Mode Doberty Power Amplifier

Voravit Vorapipat, Cooper Levy, Peter M. Asbeck
University of California, San Diego, USA
RMO4D-4 16:40

A Fully Integrated Software-Defined FDD Transceiver Tunable from 0.3-to-1.6GHz

Dong Yang¹, Hazal Yüksel², Christopher Newman², Changhyuk Lee³, Zachariah Boynton², Noman Paya⁴, Miles Pedrone⁵, Alyssa Apsel², Alyosha Molnar²
¹Broadcom, USA, ²Cornell University, USA, ³Columbia University, USA, ⁴Texas Instruments, USA, ⁵IBM, USA
RTU2A-3 10:50

Sunday, 22 May 2016

17:30–18:00

Marriott Salons 7–8

A Fully Integrated 320pJ/b OOK Super-Regenerative Receiver with -87dBm Sensitivity and Self-Calibration

Vahid Dabbagh Rezaei¹, Stephen J. Shellhammer², Mohamed Elkholy¹, Kamran Entesari¹

¹Texas A&M University, USA, ²Qualcomm, USA

RMO4A-2 16:00

A 28GHz Quadrature Fractional-N Synthesizer for 5G Mobile Communication with Less Than 100fs Jitter in 65nm CMOS

W. El-Halwagy¹, A. Nag¹, P. Hisayasu², F. Aryanfar², P. Mousavi¹, M. Hossain¹

¹University of Alberta, Canada, ²Samsung, USA

RM02C-1 10:10

A 5GHz All-Passive Negative Feedback Network for RF Front-End Self-Steering Beam-Forming with Zero DC Power Consumption

Min-Yu Huang, Taiyun Chi, Hua Wang

Georgia Institute of Technology, USA

RM01D-3 08:40

A 42mW 26–28GHz Phased-Array Receive Channel with 12dB Gain, 4dB NF and 0dBm IIP3 in 45nm CMOS SOI

Umut Kodak, Gabriel M. Rebeiz

University of California, San Diego, USA

RTU2C-3 10:50

A 28-GHz 4-Channel Dual-Vector Receiver Phased Array in SiGe BiCMOS Technology

Yi-Shin Yeh¹, Benjamin Walker², Ed Balboni², Brian A. Floyd¹

¹North Carolina State University, USA, ²Analog Devices, USA

RTU2C-4 11:10

0.84-THz Imaging Pixel with a Lock-In Amplifier in CMOS

Rui Xu¹, Ja-Yol Lee², Dae Yeon Kim¹, Shinwoong Park³, Zeshan Ahmad¹, Kenneth K. O¹

¹University of Texas at Dallas, USA, ²ETRI, Korea, ³Virginia Tech, USA

RM03A-3 14:10

Student Paper Contest Eligibility: The student must have been a full time student (9 hours/term graduate, 12 hours/term undergraduate) during the time the work was performed. The student must be the lead author of the paper and must present the paper at the Symposium. A memorandum will be automatically sent to the advisor to certify that the work was done by the student.

Judging Procedure: Student papers are reviewed and admitted to the conference in the same manner as all other conference papers. After the paper review process is completed, each technical program subcommittee recommends a maximum of two student papers as finalists. A student paper contest committee consisting of one representative from each subcommittee is then formed to review all the finalists and select the top three papers. Papers accepted for the competition are judged on content.

Stefano Pellerano
Student Programs Chair
Intel